

**ATM reassembly controller and method**

DA Thompson - US Patent 5,917,826, 1999 - Google Patents  
 ... 006-11 PAD1 GPCH - 0011 LENGTH1 CRC - 32 PAYLOAD CFI 0-65535 0-47 1 1 2 4 / 620 -  
 CPCH TRAILER 80- PROCL - \*P) DETERMINE VCN FROM VPC AND VCI - 1204 FETCH RCITE  
 TO DETERMINE VC 1208 1210 - 1206 LOCAL MEMORY POINTER VALID (STATE 1 ...

Classified by: A1 - Related articles - All 2 versions

**Method and apparatus for dynamic packet batching with a high performance network interface**

S Mueller - US Patent 6,453,204, 2002 - Google Patents  
 ... HEADER 210 LAYER TWO HEADER 212 LAYER THREE HEADER 214 LAYER FOUR HEADER  
 216 DATA PORTION 202 TRAILER 206 PACKET ... 19,2002 Sheet 14 of 49 US 6,483,804 B1  
 PACKET QUEUE 116 PACKET # ENTRY 800- READ POINTER 810 WRITE POINTER ...

Classified by: H4 - Related articles - All 4 versions

**Apparatus and method of analyzing internet activity**

Tim Creevey - US Patent 6,287,253, 1998 - Google Patents  
 ... Destination: 00:00:94:05:69:00 5744 Source: 08:00:07:3c:00:46 Protocol Type: 0x0800 IP  
 Header - Internet Protocol ... Number: 1155984753 Offset: 5 Reserved: 00000000 Code: 0011000  
 Ack is valid | Push Request Window: 11557 Checksum: 0x5ee4 Urgent Pointer: 0 No ...

Classified by: H4 - Related articles - All 2 versions

**Table driven programming system for a services processor**

AE Henderson - US Patent App. 10/308,041, 2003 - Google Patents  
 ... 18,2003 Sheet 6 of 7 US 2003/0231634 A1 CN CD A Co T CM c d c C o g to g to CD to Q Z3  
 CO - A - CN CD CD Q Q CD CD T - CN - C C CD g o g 2 to to E T A C D CD CD  
 - z CO CD A CN CO Q Q c g C O c g 1 - k - CD CD CD 3 E E E N E L P p o ...

Published articles - All 3 versions

**Method and apparatus for data re-assembly with a high performance network interface**

S Muller, DE Gerdny Jr & T Cheng - US Patent 6,480,459, 2002 - Google Patents  
 ... ONE HEADER 210 LAYER TWO HEADER 212 LAYER THREE HEADER 214 LAYER FOUR  
 HEADER 216 DATA PORTION 202 TRAILER 206 PACKET 1 200 ... 12,2002 Sheet 14 of 49 US  
 6,480,459 B1 ENTRY 80 READ POINTER 810 WRITE POINTER 812 PACKET QUEUE 116 ...

Classified by: H4 - Related articles - All 3 versions

**Methods and devices for transmitting data between storage area networks**

A Goshani, N Mehta, P Krishnamoorthy - US Patent App. 10/ ... 2003 - Google Patents  
 ... PS p re o 2 N x O O s c r re o R v O k - Ji ... 10 illustrates a remaining pointer stored in the  
 transmit queue after a packet has been sent but before an ACK has ... In this example, card 115  
 has added FCIP frame header 115, TCP header 220 and IP header 225 to form FCIP ...

Published articles - All 3 versions

**[PDF] Understanding network processors**

N Shah - Master's thesis, University of California, Berkeley, 2001 - Citeseer  
 ... this, AAL5 defines its own Packet Data Unit (PDU) with the following contents [6]: Payload: Higher  
 layer PDU, maximum 65,535 bytes Padding: for AAL5 PDUs to evenly fit into some number of  
 ATM cells 8-byte trailer: o User-to ... Figure 7: Internet Protocol (IP) Header Format. ...

Classified by: D2 - Related articles - All 23 versions

[PDF] from cs.berkeley.edu

**[PDF] Proknet c@: Not your average IP over ATM segmenter**

R Moxon - 2000 - Citeseer  
 ... Instruction Set Computer SAP Service Access Point SAR Segmentation and Reassembly SP  
 Stack Pointer SPR Special ... Thus the SAR has to group any SAR headers and/or trailers as well  
 as the CS ... to the spec and on the receiver end CRC will be performed on Trailer and The ...

Related articles - View as HTML - All 2 versions

[PDF] from psu.edu

**[PDF] Using Time Division Multiplexing to support Real-time Networking on Ethernet**

H Sampathkumar - 2002 - Citeseer  
 ... Page 1. Using Time Division Multiplexing to support Real-time Networking on Ethernet  
 by Hariprasad Sampathkumar BE (Computer Science and Engineering), University  
 of Madras, Chennai, 2002 Submitted to the Department ...

Classified by: D2 - Related articles - View as HTML - Library Search - All 13 versions

[PDF] from psu.edu

**Static data item processing**

BS Brakken - US Patent App. 08/944,092, 2001 - Google Patents  
 ... 304 300 312 \* 2nd Processing Module 1 320 328 336\* I Output Page 2. Patent Application  
 Publication Mar. 6,2003 Sheet 1 of 21 US 2003/0046429 A1 Input Output 11 100 Packet Memory  
 102 Memory Interface 104 Processor A17 106 Processor Memory FIG. ...

All 2 versions

**Dynamic data item processing**

BS Brakken - US Patent App. 08/944,297, 2001 - Google Patents  
 ... 304 300 312 320 328 Output 336\* Page 2. Patent Application Publication Mar.  
 6,2003 Sheet 1 of 21 US 2003/0043800 A1 Input Output 100 Packet Memory 102  
 Memory Interface 104 Processor 106 I7 Processor Memory FIG. ...

All 2 versions

**Table searching technique**

J Kanwar - US Patent App. 09/732,402, 2000 - Google Patents  
 ... LU 0 L1 L5 \*\* IP n v k - y O o i O C on ab < C II 1 Q S O ... i / i / i / i / IP FLOWS MICRO FLOW 17a  
 IP FLOW TABLE V N ST NEXT ADDRESS POINTER 1 V1 ... 20,2003 Sheet 4 of 6 US 2003/0037042  
 A FIG 4 DATA SEARCH START EXTRACT SEARCH KEY FROM IP HEADER OF A ...

All 3 versions

**Programmable processing engine for efficiently processing transient data**

Di Keri, PM Key, ML Whigley. - US Patent 6,513,105. 2003 - Google Patents  
 ... Page 6. US Patent Jan. 28, 2003 Sheet 4 of 10 US 6,513,108 B1 LU fy-ry ^- LU CD CD LU 01  
 CO o Q II LU - o Q iLUeg CO Q \_LU\_ ac— LO CM ex LU O o LU 1 1 O O G L C C O O - LU O o ^ ^  
 X S d I — LU CO os < Q LO ^ ^ CO o t r o CD O O CO LU < C (V u \_ co Q\_ Page 7. ...

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**Apparatus and method for using information in one direction of a bi-directional flow in a network to alter characteristics of the return direction flow**

RD Maner. - R. Desimone. - US Patent App. 10/766,994, 2002 - Google Patents  
 ... Patent Application Publication Dec. 11, 2003 Sheet 7 of 8 US 2003/0227942 A1 FIG. 7 POLICY  
 S COUNTER MAPPING 536 SIGNATURE ALGORITHM GENERATOR QOS COMPILER HPP  
 COMPILER CE SCRIPT COMPILER n^ NPE IMAGE BUILDER CONTROL I DATA ... ..

[Cited by 1](#) - [Related articles](#) - [All 2 versions](#)

**Packet processing system including a policy engine having a classification unit**

CE. Nando, K. Epi, N. Mani, P. Shankar. - US Patent. - 2000 - Google Patents  
 ... 5, 2000 Sheet 12 of 19 110 DMA Ring Base (as defined in the Ring Array diagram) Invalid  
 1112 1102 DMA RECOV (pointer register incremented ... 19 RA2 RESVEC 2BIT 1208 DIN1 W1 W0  
 W2 1 1262 BIT-ALU RES-GANG 1240 1254 MEM ADDR 1214^ \ IF x / B r r v y^ \_LUJ ...

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**Method and apparatus for end-to-end encryption of a data packet in a computer network**

WR Howe, BV Lampash. - US Patent 5,594,056. 1997 - Google Patents  
 ... 0 K 'Z' \_ II t x " " o ^ c n g ^ 38 E c n u a ^ ^ 5 E - X X P n n c ^ ^ c ^ D ^ c n P ^ x ^ ^ i  
 O \_ \_ Z ^ ~ X Q j a e t o x ^ T — u l 2 u \_ X Z \_ J F - X ^ s U J r v I I ^ c n z I L Z ^ V ^ 5 h ^ 1 . - | 1 \_  
 a ^ O O o : 1 O o o o ^ ~ | 57 m a ^ o o K C M 1 - o o ^ o o T — a ^ a \_ ...

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